

```
01 LBL "ROM" ;Direct Addressing of ROM Routines
02 SF 01
03 STO M
04 CLX
05 RCL b
06 X<> M
07 FC?C 01
08 RTN
09 >"ABCDE"
10 X<> M
11 STO a
12 X<> N
13 X<> M
14 ARCL 10
15 X<> M
16 STO N
17 CLX
18 X<> a
19 X<> M
20 >"AB"
21 STO M
22 X<> N
23 STO b
24 END
```

```
003AC000F400524F4DA801917577907CCE75AB0185F67F4142
434445CE75917BCE76CE759B0ACE75917677CE7BCE75F37F41
429175CE76917CC0000D27
```

58 BYTES

HP-41C Direct ROM Addressing by William C. Wickes PPC V7 N5 P55 June 1980

Program Registers Needed: 9

Row 1 (1 - 5)



Row 2 (5 - 9)



Row 3 (10 - 16)



Row 4 (16 - 22)



Row 5 (22 - 24)

